

FIG. 3A

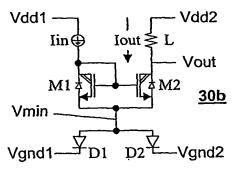


FIG. 3B

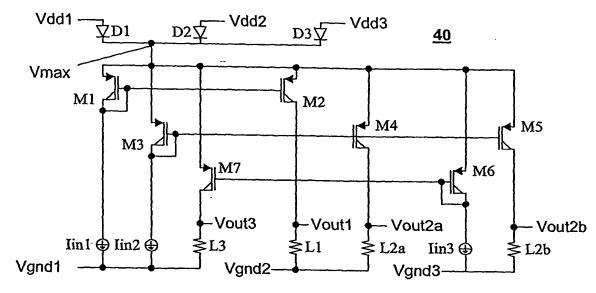


FIG. 4

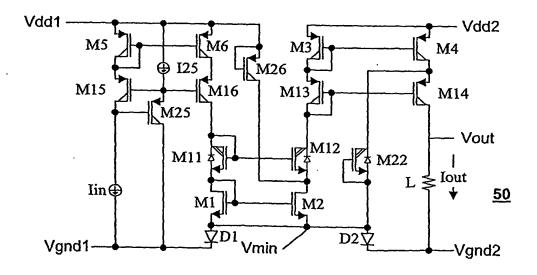


FIG. 5



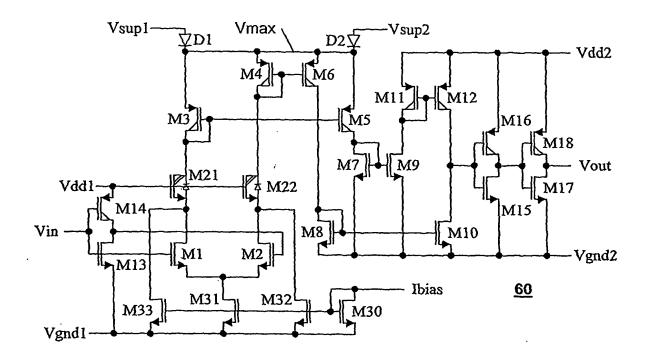


FIG. 6

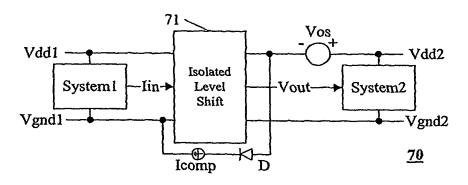
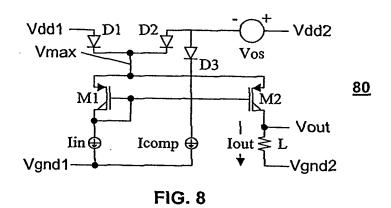


FIG. 7



Vsup1-Vmax Vsup2 立DI D2⊈ Vdd2 M6 M11 M12 <u>D40</u> 🕈 M5 M16 **M**3 M18 M7 M9 Vout M21 M22 M41 Vdd1 M14 M15 M10 Iin/Vin-M8 M2 MI -Vgnd2 M13 M40 <u>90</u> - Ibias M32 M31 М33 M30 Vgnd1

FIG. 9

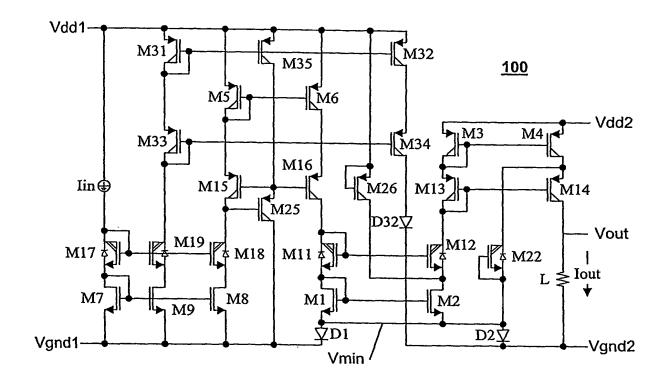


FIG. 10

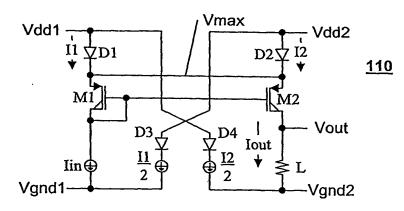


FIG. 11

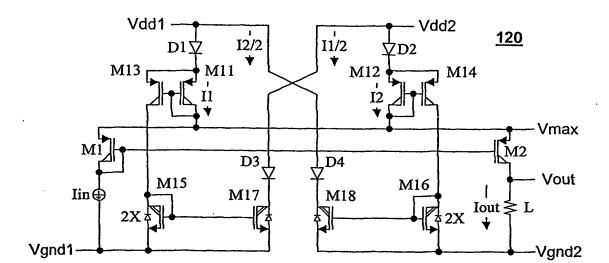
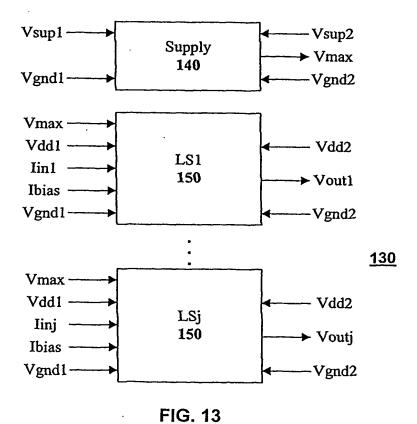
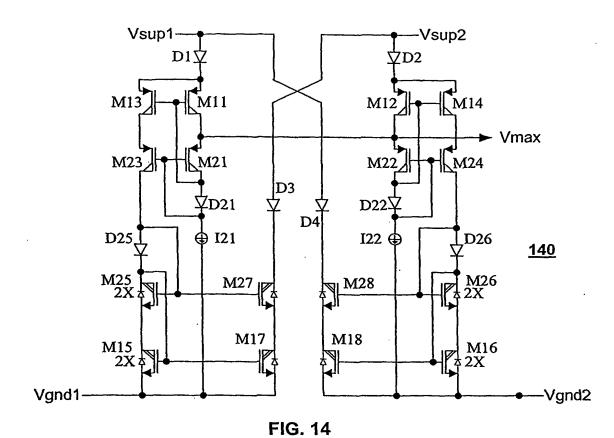


FIG. 12





Vmax Vdd2 M6 M11 | M12 M5 M3 M16 M18 Vout M7 M21 M22 M17 Vdd1-M14 M15 M10 M8 Iin/Vin-M2 -Vgnd2 M1. IL→M13 Ibias <u>150</u> M31 M32 M33 M30 Vgnd1 FIG. 15

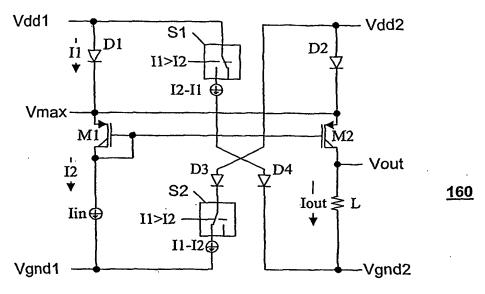


FIG. 16

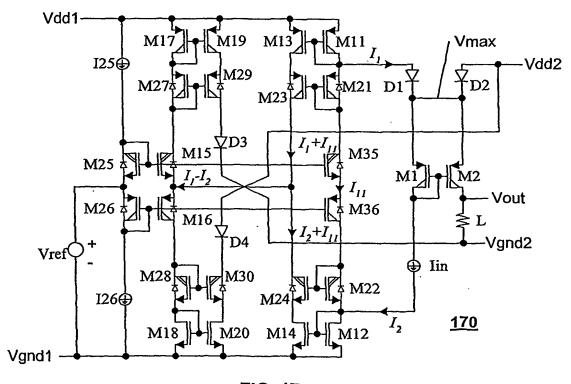


FIG. 17